

Amendments to the Specification:

Please replace paragraph [026] with the following amended paragraph:

a1 [026] BIST components of the generator 10 include a BIST state machine 46, lookup tables for BIST 48, a checksum selection module 50, a checksum comparator 52, and a BIST register 54[[]], and a BIST state register 56.

Please replace paragraph [059] with the following amended paragraph:

a2 [059] **Figure 10** is a block diagram illustrating an arrangement by which a complete test of transmit, channel and receive units of an exemplary communications device 340 may be performed. Specifically, for functional testing, a set of patterns that are verifiable by an observation system must be determined. To commence a complete test of the device 340, the device 340 is placed in a pattern generation mode by assertion of an appropriate signal as the test mode number input signal 280. A suitable clock is applied as the system clock 16, and the output of a receiver of the device 340 is observed and monitored 342. In one embodiment, no additional test equipment is required. The observing system may perform any operations that are suited to decoding the received data, such as a straight forward comparison of the supplied and received patterns. Other methods of verification utilize a visual verification (e.g., for video channels), control systems (e.g., where the transmitted pattern instructs an observing unit to perform task) and data analysis systems (e.g., where the stream and/or packets of test data are evaluated to determine the validity thereof).